

**Department Computer Science and Engineering**

**V Sem CSE-C3 (2024 - 25)**

**Computer Networks - Assignment 1**

**Date: 11. Sep. 2024**

1. Hoe do you deal the packet loss and Acknowledgement loss in all stop and wait ARQ? And analyse the pros and cons among all ARQ mechanisms in real world applications. **CO2, BL4**
2. Hardware limitation interrupts to handle high volumes of real-time data efficiently why? Justify. **CO2, BL3**
3. Analize the real time challenges with multiple access protocols in computer networks. **CO2, BL4**
4. Derive the derivation for pure ALOHA and slotted ALOHA and compare the scenarios. And also discuss the vulnerable time in both the protocols briefly. **CO2, BL3**
5. Assume the sender sends the dataword 1100110010 and receiver receives 1110110010 and receiver machine detects that codeword is suffering from double errors. Find the scenario of error detection and correction using hamming code. **CO2,BL4**